

Qam Modulator Fpga Ip Core Iprium

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Qam Modulator Fpga Ip Core

The QAM Modulator and Demodulator IP cores, provided by UTS are FPGA proven high data rate QAM IP solution. These IP cores can use for modulating data signals onto a carrier used for radio communications. QAM is a signal in which two carriers shifted in phase by 90 degrees are modulated and the resultant output consists of both amplitude and phase variations.

UTS - QAM Modulator and Demodulator IP core

QAM Modulator IP Core is sample 32 bandwidth control (symbol rate): 0.01% to 25% of iclk odati W_DAC modulator output at baseband (I channel) or at an intermediate frequency odatq W_DAC modulator output at baseband (Q channel) ordy 1 ready to accept input data IP Core Parameters

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Table 2 describes the QAM Modulator IP Core parameters, which

QAM Modulator FPGA IP Core - IPrrium

Modulators / Demodulators. The IP Core implements the QAM modulation with filtering and signal interpolation. Mapper Pulse Shaping Filter Resampler Quadrature Modulator NCO DDS Version : 4.0 Build date : 2014.09 Ordering code : ip-qam-modulator Supported technologies : FPGA (Xilinx, Intel/Altera, Lattice, Microsemi/Actel), ASIC (Digital ASIC)

QAM Modulator IP Core - FPGA IP Cores, wireless modems, FEC

QAM Modulator FPGA IP-Core DVB-C Modulation According to ETSI-EN 300429 V1.2.1 • Constellations: QAM16, QAM32, QAM64, QAM128, QAM256 • Symbol rate: 1000-7000 ksym/s • Implementation using a single 27MHz crystal • Integrated IF upconverter and interpolation filter • IF output adjustable between 3.5MHz and 70MHz

QAM Modulator FPGA IP-Core - maintech

Modulators / Demodulators. The IP Core implements the QAM signal demodulation using Costas and Gardner schemes. Quadrature Demodulation Decimator/ Resampler Matched Filter Decision Slicer DDS Recovery External AGC Version : 3.0 Build date : 2014.09 Ordering code : ip-qam-demodulator

QAM Demodulator IP Core - FPGA IP Cores, wireless modems, FEC

The IP Core fully supports all Xilinx and Altera FPGA families, including Spartan, Zynq, Artix, Kintex, Virtex, Cyclone, Arria, MAX, Stratix. Table 3 summarizes the QAM Demodulator IP Core measurement results. Table 3. The QAM Demodulator performance IP Core parameters FPGA type Resource Speed grade, maximal system frequency

QAM Demodulator FPGA IP Core - IPrrium

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The J.83B cable modulator modulates an MPEG-TS DVB-SPI input into a QAM-16/256 output in baseband I/Q. Description The MVD Cable modulator J83B core is a drop-in module for FPGA that includes the following functions:

QAM modulator - J.83 Annex B - IP core for FPGA

Excellent MER and shoulder attenuation values are key advantages of maintech's multiple FPGA QAM modulation with DUC. Short-term evaluation platforms: Xilinx Virtex-6 LX130T Evaluation Kit by AVNET Analog Devices AD9739-CMTS-EBZ Evaluation Kit (see fig.) IP Core.

16ch QAM & DUC FPGA IP-Core - Jordaan Electronics

The MVD DVB-C Modulator J.83 Annex A/C core is a drop-in module for FPGA that includes the following functions: Input data framer from DVB-SPI source (MPEG-TS flow) J.83AC modulator: Energy dispersal; Reed Solomon encoder; Interleaver; QAM symbol mapper; Output for complex DAC (2 x 8 bits) Features

QAM modulator - DVB-C J.83 Annex A/C modulator - IP core ...

COFDM modulator - DVB-T - IF output DVB-T/H modulator - IF output ... The MVD DVB-T/H modulator is a drop-in module for FPGA that includes the following functions : Input data framer (or SFN FIFO) from DVB-SPI source (MPEG-TS flow) ... 16-QAM and 64-QAM Symbol Mapping; Configurable support for 2k, 8k and 4k (DVB-H) OFDM modes ...

COFDM modulator - DVB-T - IF output - IP core for FPGA

The IPrium-DVB-C-Modulator IP Core implements the modulation standard ETSI EN 300 429 V1.2.1 DVB-C. The IP Core contains Scrambler, RS Encoder, Differential Encoder, QAM Modulator (16-QAM, 32-QAM, ...

Modulation/Demodulation IP Core - Design And Reuse

FPGA-based platforms implementing these IP cores are also available. License ... Complete VHDL/IP Core license agreement; MODULATORS-ONLY. Part number Description Specifications Price Check availability ... 67KB: \$750 : COM-1402SOFT: PSK/QAM/APSK modulator Symbol rate up to fclk/4: 117KB: \$500 : COM-1827SOFT CPM MOD: CPM modulator FSK,MSK,GFSK ...

FPGA/VHDL/IP cores

The IP Core contains Scrambler, RS Encoder, Trellis Encoder, 64-QAM/256-QAM Modulator. 21 IP Provider : Give the best exposure to your IPs, by listing your products for free in the world's largest Silicon IP catalog (6 500 products from more than 400 companies)

Quadrature Amplitude Modulation IP Core - Design And Reuse

The QAM Modulator IP Core is available immediately in synthesizable Verilog or optimized netlist format, along with synthesis scripts, simulation test bench with expected results, and user manual. For further information, product evaluation, or pricing, please visit the IP Core page: QAM Modulator IP Core

IPrium adds 128-APSK and 256-APSK support to QAM Modulator ...

The MVD ATSC core is a drop-in module for FPGA that includes the following functions: Input data framer from DVB-SPI source (MPEG-TS flow) ... The ATSC modulator core is delivered for baseband output to be natively connected to AD9789 DAC from Analog Devices. When associated to our Up Sampler IP core, ...

ATSC 8VSB modulator - IP core for FPGA

The IP Core fully supports all Xilinx and Altera FPGA families, including Spartan, Zynq, Artix, Kintex, Virtex, Cyclone, Arria, MAX, Stratix. Table 4 summarizes the RS-QPSK Modulator IP Core

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measurement results. Table 4. The RS-QPSK Modulator performance IP Core parameters FPGA type Resource Speed grade, maximal system frequency

RS-QPSK Modem FPGA IP Core - IPrium

High Performance VSB/QAM Demodulator Core Full digital demodulator supports 8-VSB and 64/256-QAM. Fully separable core for optional OOB forward data channel. Prototyped in FPGA and tested in the lab and in the field by independent...

Xilinx Quadrature Amplitude Modulation IP Core

For custom hardware developments, the use of an IP core offers the chance to save development time and opens the possibility of using existing hardware in new applications. The maintech IP cores for FPGA modulation are especially suited for this as they were developed with special attention on the following aspects:

DVB IP-Cores - maintech Homepage: MultiFM Modulator

High Performance VSB/QAM Demodulator Core Full digital demodulator supports 8-VSB and 64/256-QAM. Fully separable core for optional OOB forward data channel. Prototyped in FPGA and tested in the lab and in the field by independent...

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