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LUPA1300-2 is an
integrated SXGA high
speed, high sensitivity

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CMOS image sensor.
This sensor targets
high speed machine
vision and industrial
monitoring
applications. The
LUPA1300-2 sensor
runs at 500 fps and has
triggered and pipelined
shutter modes. High-
speed CMOS ring
oscillators with low
supply sensitivity

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High Speed High Sensitivity Cmos HCU stands for high-speed CMOS un-buffered. This type of CMOS contains no buffer and is ideal for crystals and other ceramic oscillators needing linearity.

VHCMOS, or AHC, stands for very high-speed CMOS or advanced high-speed CMOS. Typical propagation delay time is between 3 ns and 4 ns. HCMOS - Wikipedia

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HCMOS ("high-speed CMOS") is the set of specifications for electrical ratings and characteristics, forming the 74HC00 family, a part of the 7400 series of integrated circuits.

[1] The 74HC00 family followed, and improved upon, the 74C00 series (which provided an alternative CMOS logic

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family to the 4000 series but retained the part number scheme and pinouts of the standard 7400 series (especially the 74LS00 series) .

HCMOS - Wikipedia

High-speed transient image sensors with frame rates as high as 10 M-frames/s, combined with 13-bit resolution, has been achieved using on-focal-plane frame storage.

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Fabricated in a standard digital 0.35 micrometer digital CMOS technology, the latter device includes a photodetector, a charge-integrating amplifier and an array of 64 sample circuits per pixel.

High-speed high-sensitivity low-noise scientific CMOS ...

With an electronic shutter and all pixel progressive readout of

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120 fps, the Canon 3U5MGXSBA 5 MP CMOS sensor offers fast, distortion free image capture. In a 2/3" format, proprietary pixel designs with low power consumption and enhanced sensitivity further improve performance. This CMOS sensor is available in monochrome (3U5MGXSBAM), color (3U5MGXSBAC) and a specialized RGB-NIR

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color filter array
(3U5MGXSBAI).

5 Megapixel High Speed CMOS Sensor | Canon USA

Since it is used CMOS technology, the lower power consumption of sensor does not require cooling system as other high sensitivity image sensor technologies (ex, EMCCD, sCMOS) are required. The vertical windowing function is

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able to make frame rate faster for reduced image area. Download brochures here.

BT130C_ver2a.pdf.

Making the invisible Visible | Super High Sensitivity

As the resolution of CMOS image sensors advances, the demand for higher AF performance becomes stronger, so that importance of image plane AF technology

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will be greater. By using our proposed all-pixel image plane PDAF CIS, no sensitivity degradation occurs, and high speed and accurate AF can be realized on the entire image plane.

High-definition and high-sensitivity CMOS image sensor

...

Andor's Fast and Sensitive sCMOS Cameras. Andor's

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Scientific CMOS (sCMOS) cameras series deliver an advanced set of performance features that render them ideal for high fidelity, quantitative scientific measurements.

Providing a wide gamut of application advantages across the biological sciences, the multi-megapixel cameras offer a large field of view and high resolution, without

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compromising read
noise, dynamic range
or frame rate.

Fast and Sensitive sCMOS Cameras - Andor - Oxford Instruments

AM41 is a high
sensitivity high speed
CMOS sensor.

4-Megapixel . 4/3"
optical size. High
sensitivity 9V/Lux-s.
500+ Frames/s. AM41
Data Sheet is here. For
those, who want to

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save time. building
their own camera,
Alexima. offers the
Sensor Board.

CMOS Image Sensor Company

CCD and CMOS sensors are sensitive to wavelengths from approximately 350 - 1050nm, although the range is usually given from 400 - 1000nm. This sensitivity is indicated by the sensor's spectral

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Sensor L1S

response curve (Figure 8). Most high-quality cameras provide an infrared (IR) cut-off filter for imaging specifically in the visible spectrum. These filters are sometimes removable for near-IR imaging.

Imaging Electronics 101: Understanding Camera Sensors for

...

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Sensitivity - High

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Definition: USB 3.0 or GIGE 1280x720 Color Camera at 85 FPS. High definition (1280x720) at 60FPS, or up to 160FPS high speed video capture at lower resolutions! Low-lux color sensor for indoor work. USB3.0 or GIGE interface.

**Extreme Speed -
High Sensitivity -
Medium Definition:
USB ...**

Abstract This paper

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presents a high-sensitivity high-speed dynamic voltage comparator, which is a key component for low power CMOS mixed signal applications. The proposed dynamic comparator employs ten transistors with only one cross-coupled latch to reduce the circuit complexity.

High-sensitivity high-speed dynamic comparator with ...

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The KURO[®] is the first back-illuminated scientific CMOS camera. With high sensitivity and fast frame rates the KURO is optimal for applications such as hyperspectral imaging, astronomy, cold-atom imaging, quantum imaging, fluorescence spectroscopy, and high-speed spectroscopy >95% QE, back-illuminated sCMOS; 1.5 e⁻ (rms) read noise;

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High UV sensitivity
**KURO | Teledyne
Princeton**

Instruments

New high-speed and high-sensitivity Linea HS TDI camera for vision applications
WATERLOO, Canada -
May 14, 2019 -
Teledyne DALSA, a Teledyne Technologies company and global leader in machine vision technology, is pleased to announce

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that its newest charge-domain CMOS TDI camera—the Linea HS—is in volume production.

New high-speed and high-sensitivity Linea HS TDI camera

...

CCDs are preferable when you want to focus on high-quality images with many pixels and need excellent light sensitivity. CMOS

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sensors are preferable for high speed cameras, as they scan and offload their footage quicker.

CCD vs CMOS - Difference and Comparison | Diffen

"The CMOS sensor is at the heart of many high-speed cameras, with key consideration to its higher number of sensor taps/channels for rapid photon-to-electron conversion to

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quickly 'rip' electrons from the sensor," says Andrew Bridges, director, sales & marketing at Photron (San Diego, CA).

Photonics Products:

High-speed Imaging:

How to choose a ...

- CMOS inverter is one of the simplest RX pre-amplifier structures
- Termination voltage, V_{TT} , should be placed near inverter trip-point
- Issues:
- Limited gain

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(<20) • High PVT variation results in large input referred offset • Single-ended operation makes it both sensitive to and generate supply noise

7

ECEN720: High-Speed Links Circuits and Systems Spring 2019

To overcome this tradeoff silicon-on-insulator (SOI) CMOS technology has been

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used to fabricate high speed, low efficiency detectors [14, 15]. One interesting effect of an insulating substrate is...

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